

Docket No.: 57454-981

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
YASUO INOUE, et al.	:	Confirmation Number: Not yet assigned
Serial No.: Cont. of Appln. Serial	:	
No. 10/062,462	:	Group Art Unit: Not yet assigned
Filed: December 24, 2003	:	Examiner: Not yet assigned
For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	:	

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Japanese Patents Laying-Open No. 04-199574, 04-14275 and 04-034980 were first cited in a corresponding foreign application office action and its relevance discussed therein. A copy

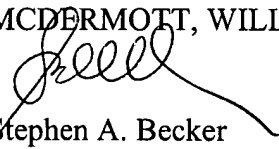
of each of these references as well as a copy of the foreign office action, together with an English language version thereof is attached for the Examiner's information. Please note Applicants did not include copies of Japanese Patents Laying-Open No. 03-187241 and 61-034978 referred to in the attached office action, since these references were cited in the parent application.

The remaining cited references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/062,462, filed February 5, 2002 , which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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Date: December 24, 2003

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 57454-981		SERIAL NO. (Continuation of Serial No. 10/062,462)	
			APPLICANT Yasuo INOUE, et al.			
			FILING DATE December 24, 2003		GROUP Not yet assigned	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,478,655	10/23/84	Nagakubo et al.			
	5,440,161	8/8/95	Iwamatsu et al.			
	4,696,092	9/29/87	Doering et al.			
	5,164,326	11/17/92	Foerstner et al.			
	5,856,695	1/5/99	Ito et al.			
	4,996,575	2/26/91	Ipri et al.			
	5,652,454	07/97	Iwamatsu et al.			
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	FOREIGN PATENT DOCUMENT <small>Country Codes - Number + Kind Codes (if known)</small>	PUBLICATION DATE	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
					Yes	No
	JP 5-182909	7/93				
	JP 53-68990	6/78				
	JP 62-104136	5/87				
	JP 03-187241	8/91				
	JP 61-034978	02/86				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	"A High Speed and Lower Power Consumption 16 K Gate Array on Ultra Thin SOI Film", by Y. Yamaguchi et al., Institute of Information and Communication Engineers of Japan, pp. 81-86, 1992.					
EXAMINER			DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

 ATTY. DOCKET NO.
57454-981

 SERIAL NO.
 (Cont. of Appln. Serial No.
10/062,462)

 APPLICANT
YASUO INOUE, et al.

 FILING DATE
December 24, 2003

 GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code ³ -Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 04-014275	1/92	Omron Corp		Partial	
		JP 04-034980	2/92	Mitsubishi Electric Corp		Partial	
		JP 04-199574	7/92	Mitsubishi Electric Corp		Partial	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.